

FIG. 1 OF 6

Block diagram

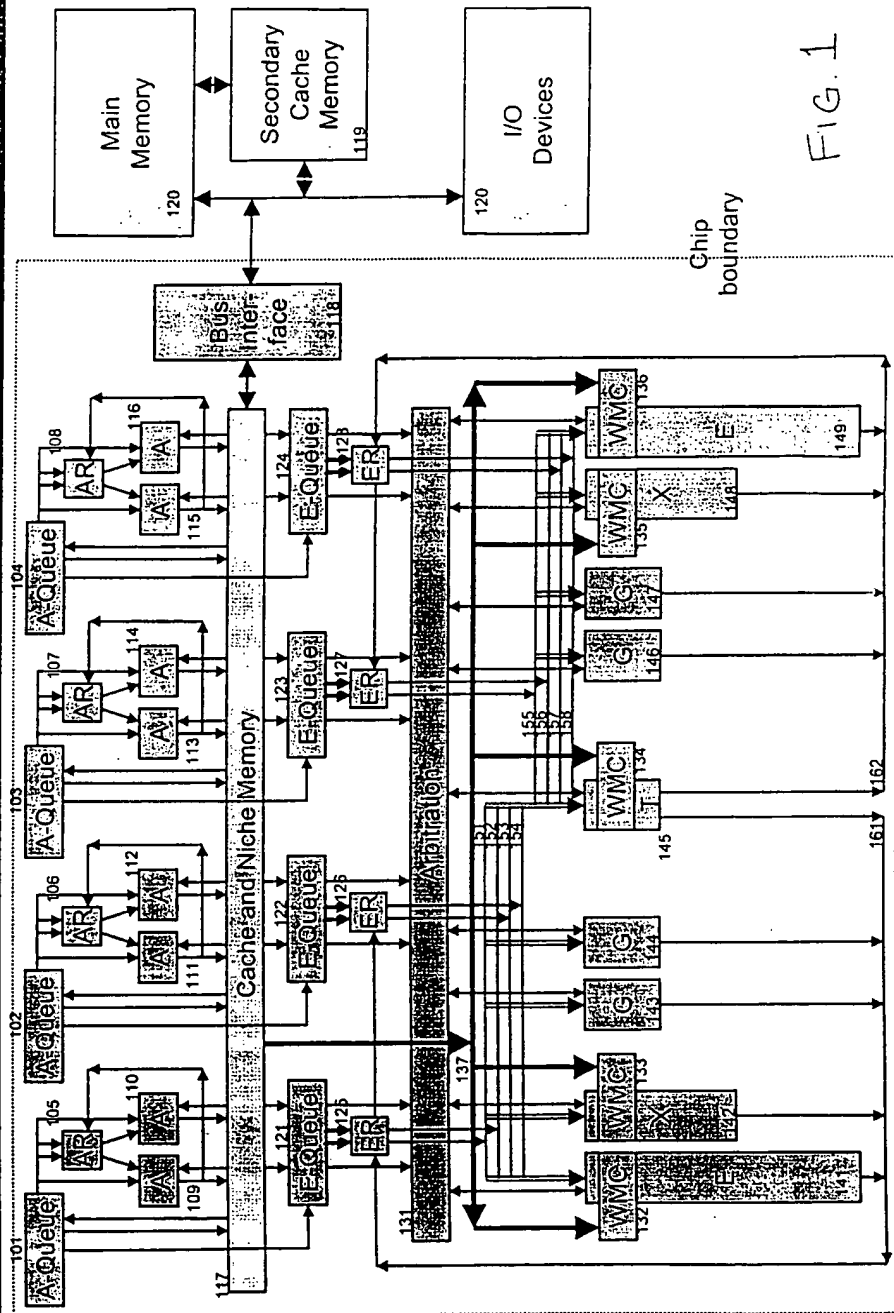


FIG. 1

FOOTNOTES

Wide multiply matrix

$$\blacksquare \text{rd}_{128} = \text{m}[\text{rc}]_{(128 \times 64/\text{size})} * \text{rb}_{128}$$

$\text{m}[\text{rc}]_{(128 \times 64/\text{size})}$

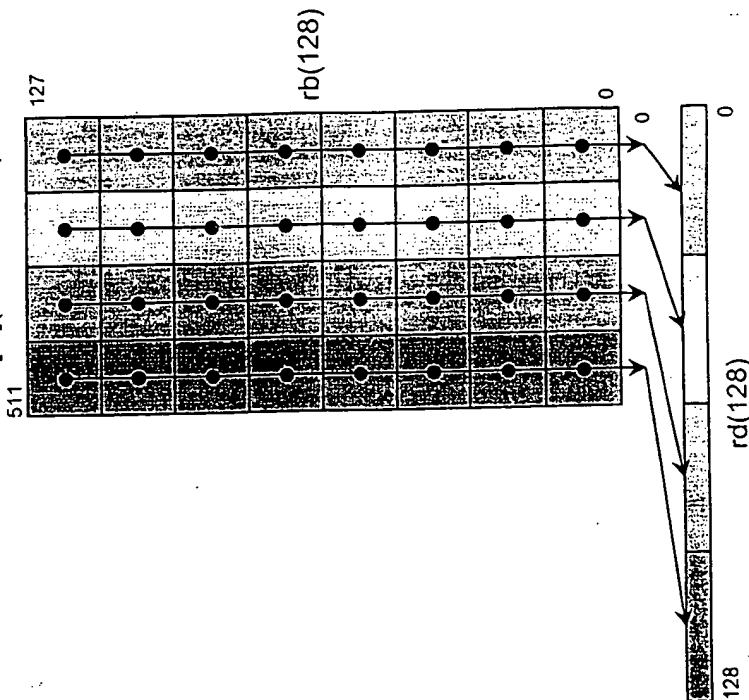


FIG. 2

Wide multiply matrix

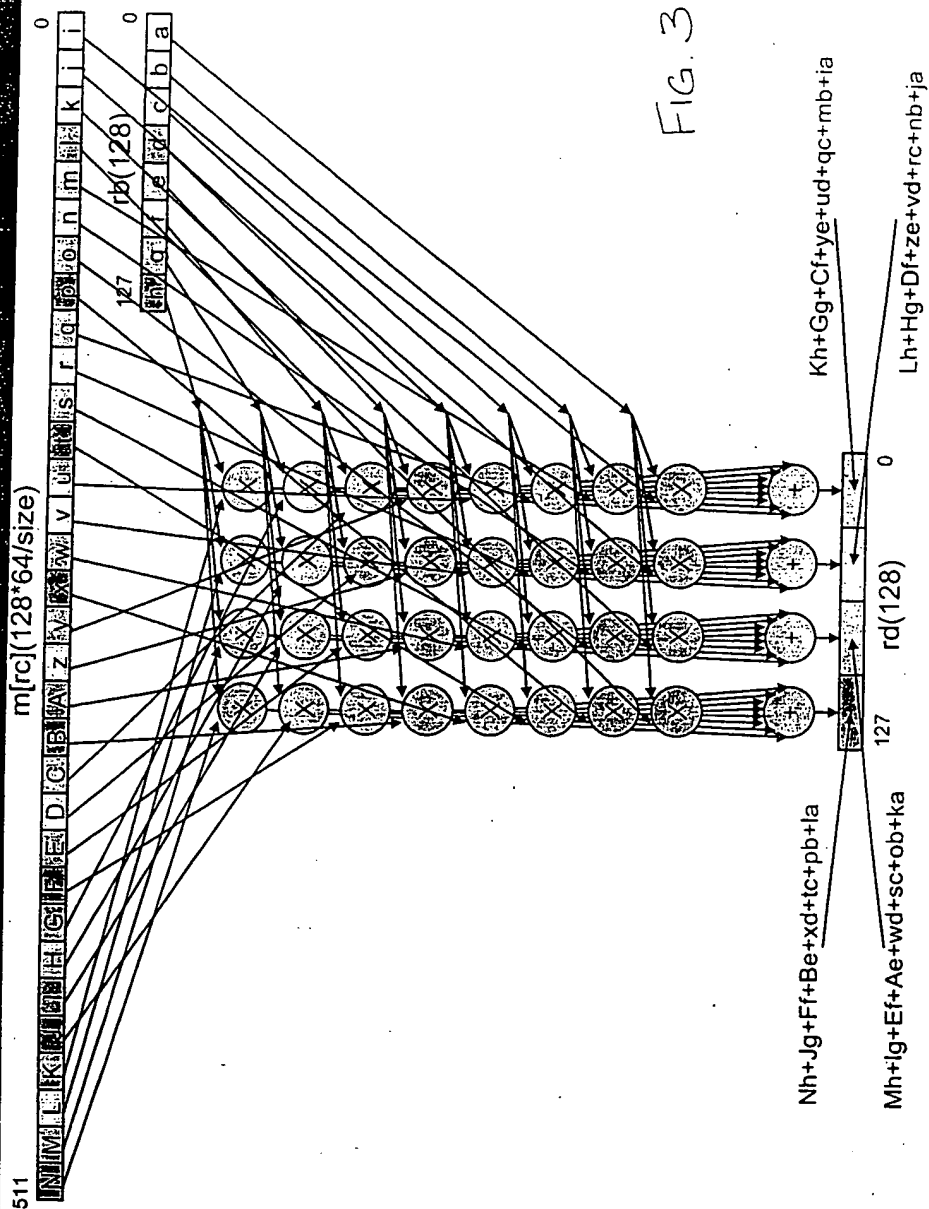
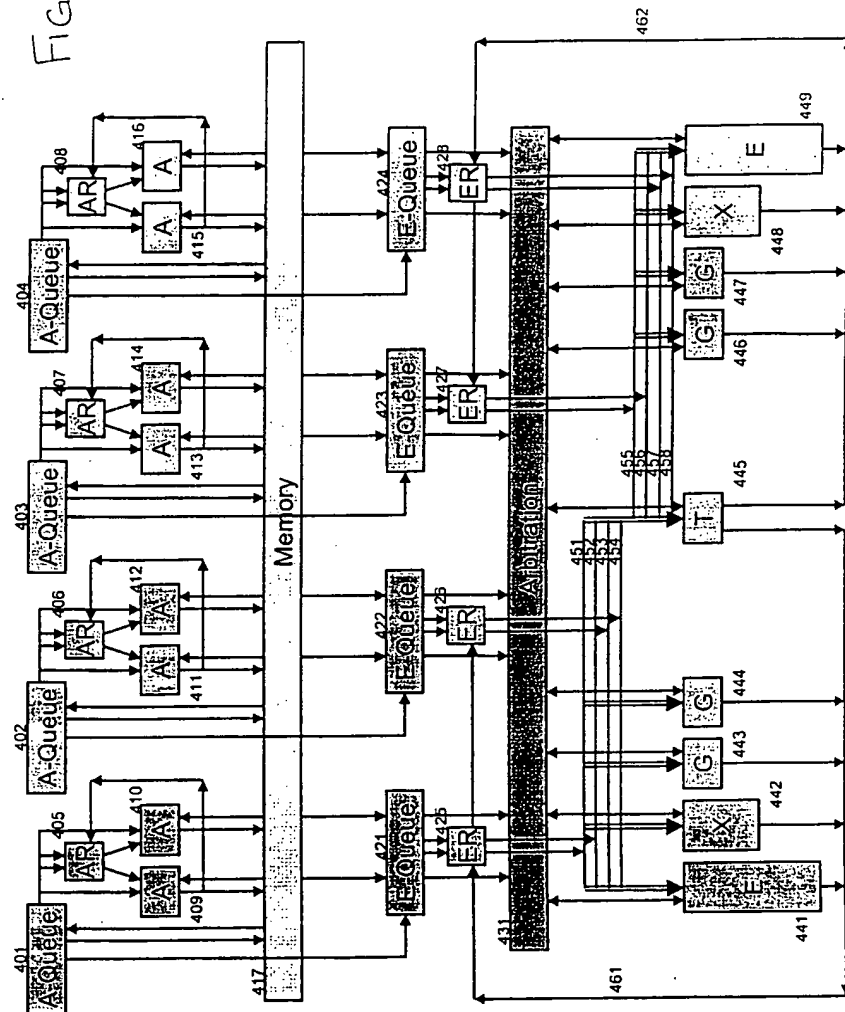


Fig. 3

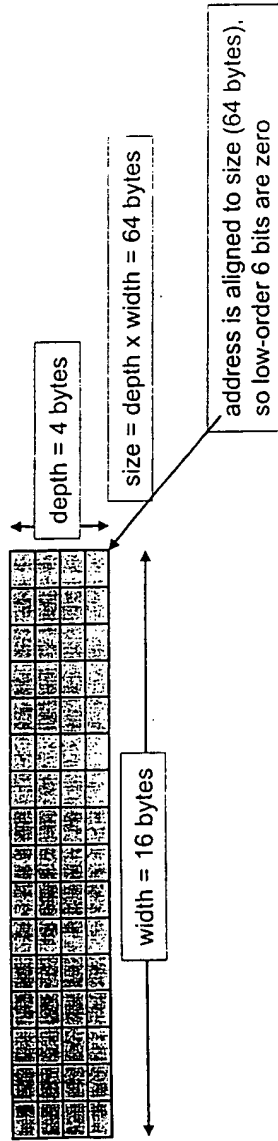
FIG. 4

SMT + DAE



Wide operand specifier

■ specifier=address+(size/2)+(width/2) FIG. 5



address

size/2

width/2

specifier

500

505

510

Specifier decoding

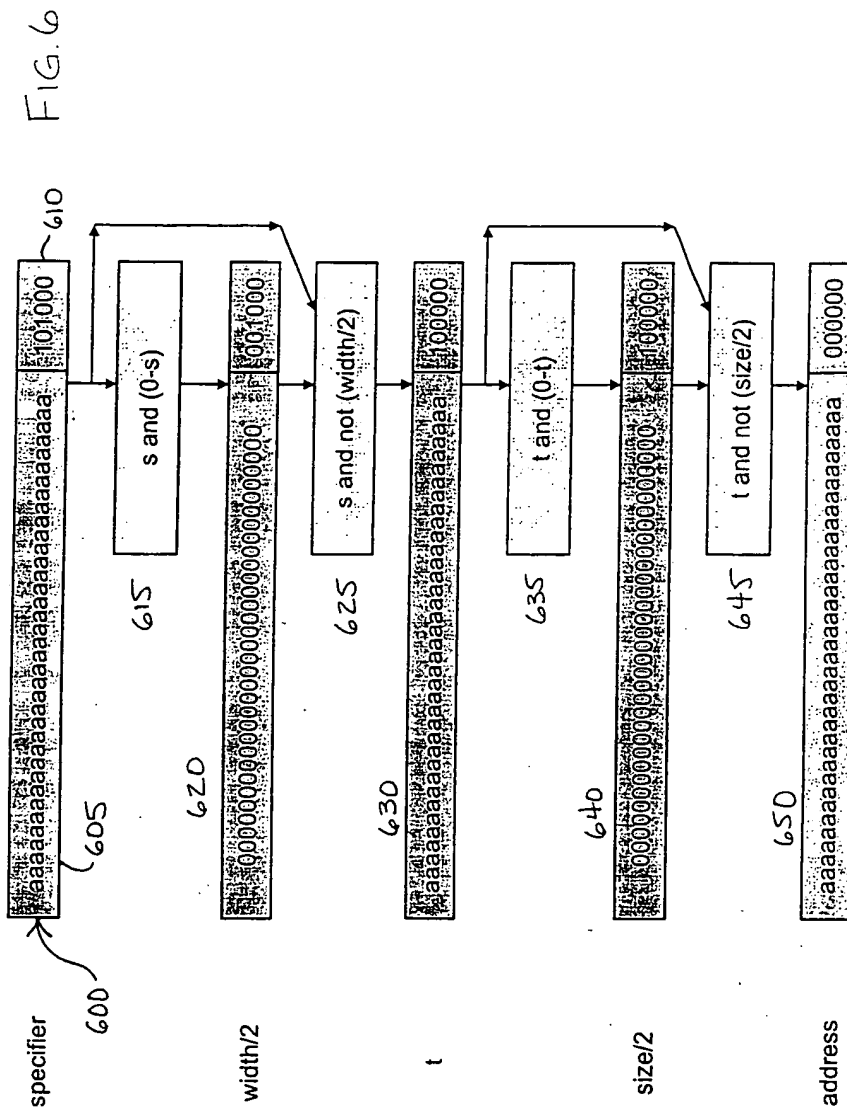
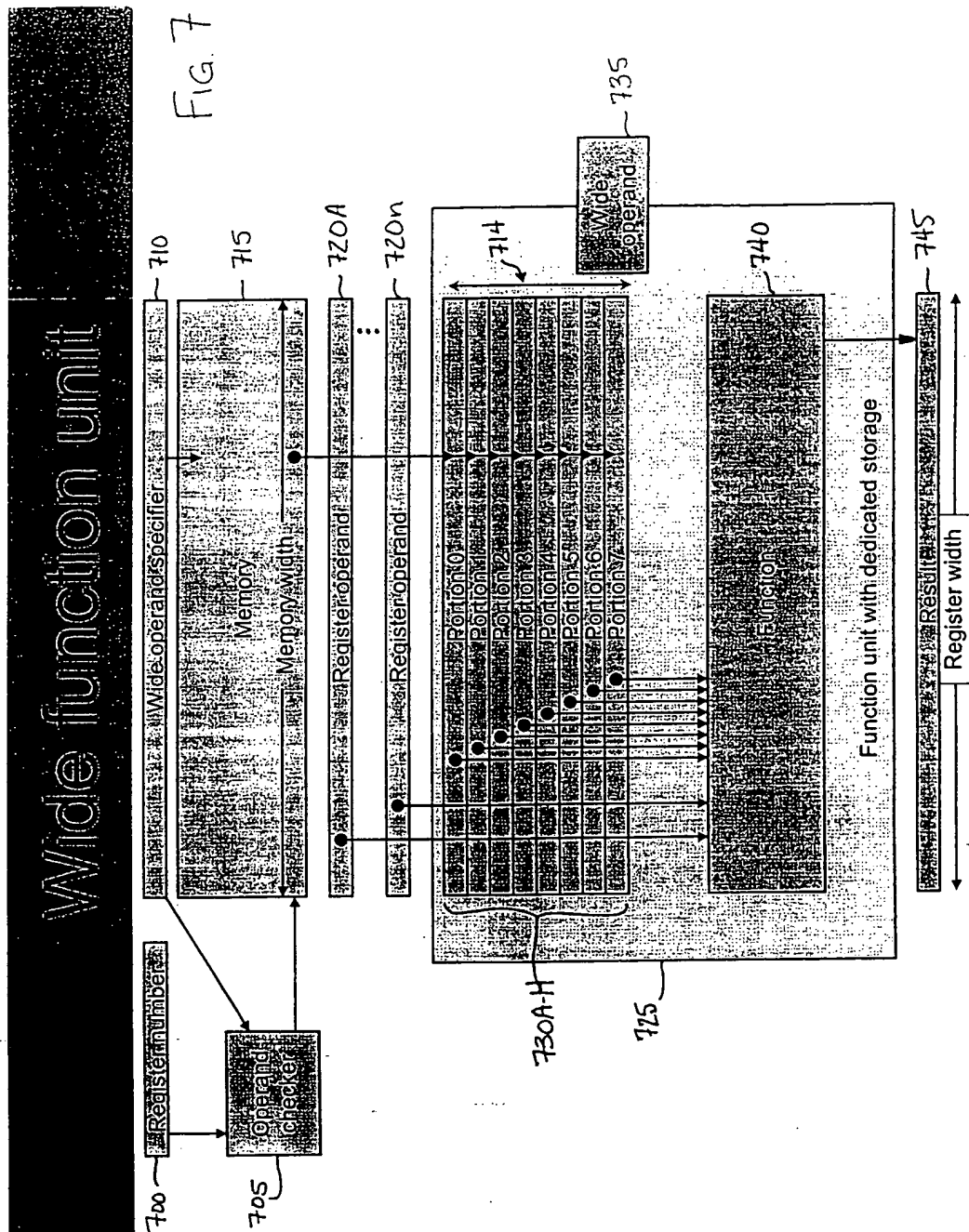
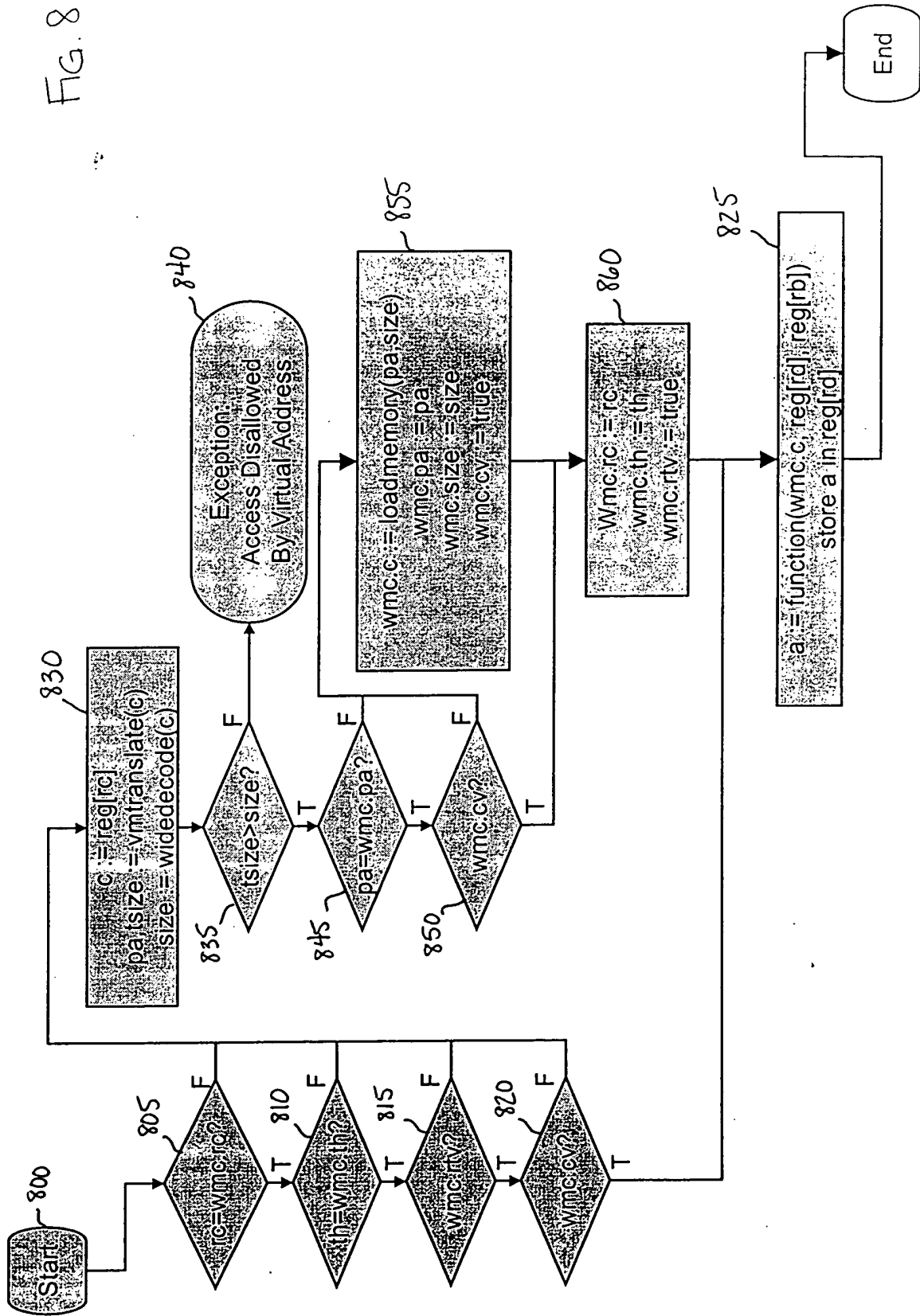


FIG. 7



Wide MicroCache control

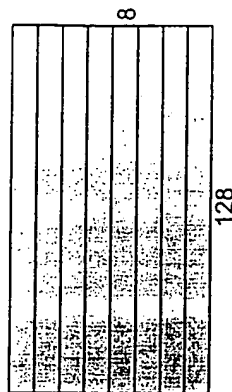
Fig. 8



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Wide MicroCache data structures

FIG. 9



■ wmc.c contents

■ wmc.pa - physical address

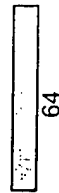
■ wmc.size - size of contents

■ wmc.cv - contents valid

■ wmc.th - thread last used

■ wmc.reg - register last used

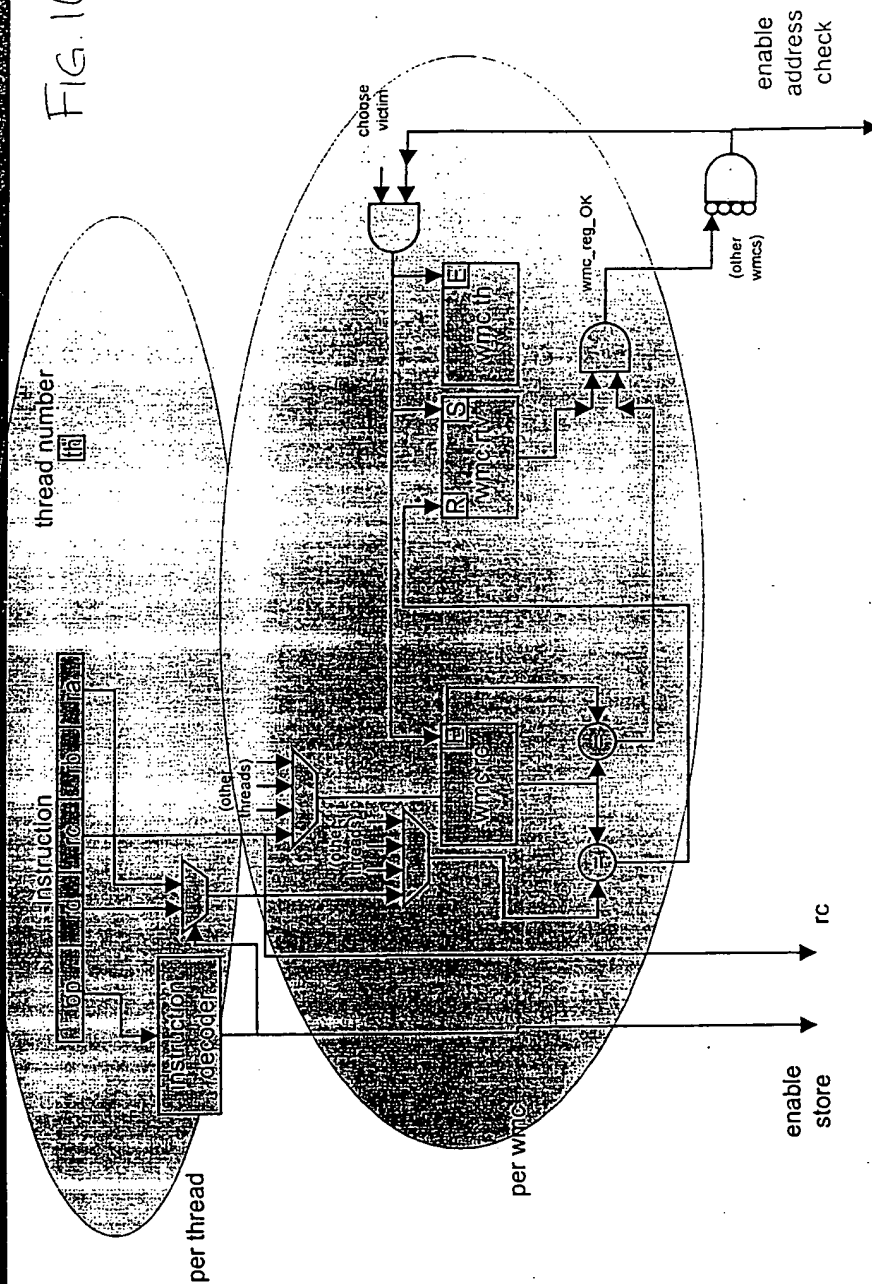
■ wmc.rtv - register & thread valid



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Wide MicroCache control (1)

FIG. 10



Wide MicroCache control (2)

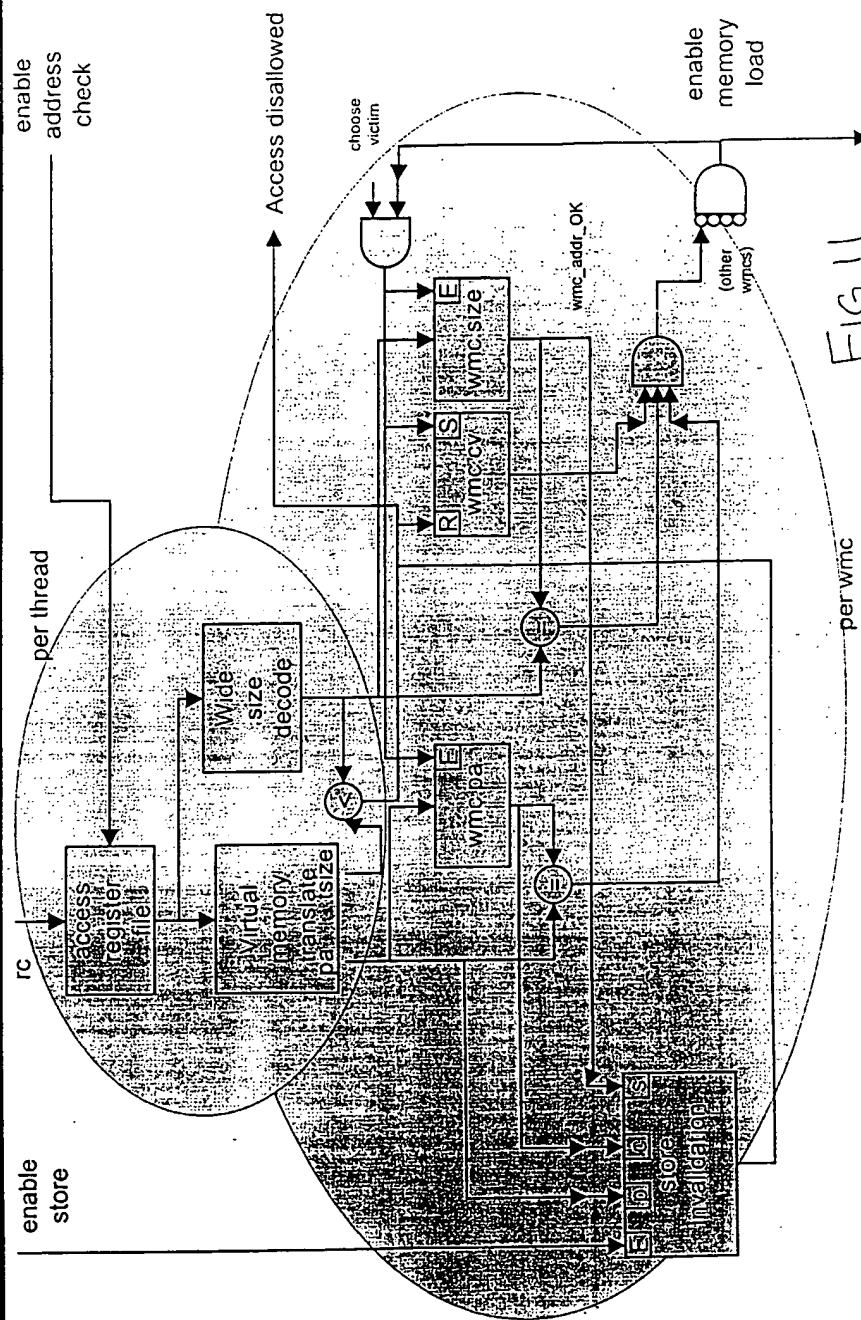


Fig. 11